

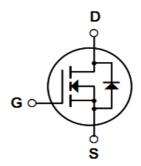
Description

This N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.

Features

- 1) V_{DS} =150V, I_D =12A, $R_{DS(ON)}$ <160m Ω @ V_{GS} =10V
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell denity trench technology for ultra R_{DS(ON)}.
- 5) Excellent package for good heat dissipation.





Ratings T_c=25°C, unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	150	V
V_{GS}	Gate-Source Voltage	±20	V
	Continuous Drain Current-	12	
I_{D}	Continuous Drain Current-Tc=100°C		А
	Pulsed Drain Current ¹	50	
EAS	Single Pulse Avalanche Energy		mJ
P_{D}	Power Dissipation	55_	W
T_L,T_STG	Operating and Storage Junction Temperature Range	-55 to +175	$^{\circ}$

Thermal Characteristics

Symbol	Parameter	Ratings	Units
Reic	Thermal Resistance, Junction to Case ²	5	00 4
R _{⊕IA}	Thermal Resistance, Junction to Ambient	-	°C/W

Package Marking and Ordering Information

Part NO.	Marking	Package
RYN150B2C	RYN150B2C	TO-251



Symbol	Parameter	Conditions	Min	Тур	Max	Units	
Off Characteristics							
BV _{DSS}	Drain-Sourtce Breakdown Voltage	V _{GS} =0V, I _D =250 μ A	150	=	-	V	
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =150V	-	=	1	μА	
I _{GSS}	Gate-Source Leakage Current	V_{GS} = \pm 20V, V_{DS} =0A	-	=	±100	nA	
On Characteristics ³							
$V_{GS(th)}$	GATE-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	1.5	2	2.5	V	
D		V _{GS} =10V,I _D =5A	-	130	160	m Ω	
R _{DS(ON)}	Drain-Source On Resistance	V _{GS} =4.5V,I _D =	-	-	-		
G _{FS}	Forward Transconductance	V _{DS} =15V, I _D =10A	-	15	-	S	
Dynamic Characteristics ⁴							
C _{iss}	Input Capacitance		-	900	-	pF	
Coss	Output Capacitance	V _{DS} =25V, V _{GS} =0V,	-	115	-		
C _{rss}	Reverse Transfer Capacitance	f=1MHz	-	70	-		
Switching	Characteristics ⁴						
t _{d(on)}	Turn-On Delay Time		-	8	-	ns	
t _r	Rise Time	V _{DS} =75V, I _D =1A,	-	10	-	ns	
t _{d(off)}	Turn-Off Delay Time	V_{GS} =10V, R_{GEN} =6 Ω	-	20	-	ns	
t _f	Fall Time		-	15	-	ns	
Q_{g}	Total Gate Charge	V _{GS} =10V, V _{DS} =75V, I _D =1.5A	-	19	-	nC	
Q_{gs}	Gate-Source Charge		-	5.5	-	nC	
Q _{gd}	Gate-Drain "Miller" Charge		-	7	-	nC	
Drain-Sou	rce Diode Characteristics						
V_{SD}	Source-Drain Diode Forward Voltage ³	V _{GS} =0V,I _S =2A	-	-	1.2	V	
t _{rr}	Reverse Recovery Time	- I _F =9.6A,di/dt=100A/ μ S	-	-	-	ns	
Q _{rr}	Reverse Recovery Charge		-	-	-	nC	

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤10 sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to product



Typical Characteristics T₁=25 °C unless otherwise noted

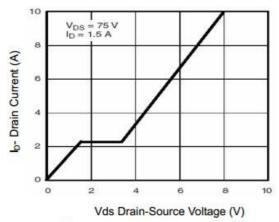


Figure 1 Output Characteristics

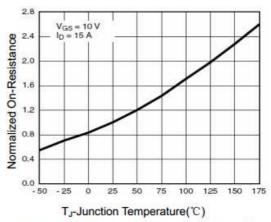


Figure 4 Rdson- Junction Temperature

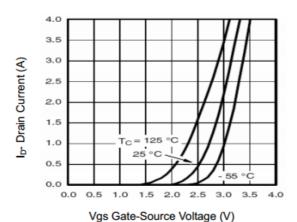


Figure 2 Transfer Characteristics

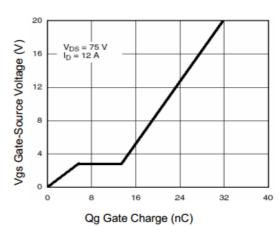


Figure 5 Gate Charge

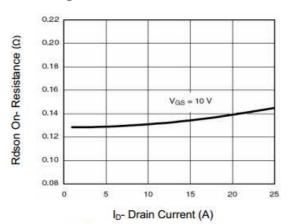


Figure 3 Rdson- Drain Current

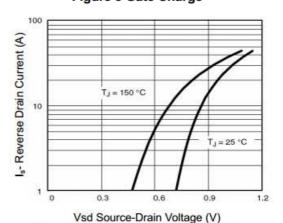


Figure 6 Source- Drain Diode Forward